



# **SMD LED**

## **Product Data Sheet**

### **LTST-C171TBKT**

Spec No.: DS22-2000-233

Effective Date: 06/22/2010

Revision: F

**LITE-ON DCC**

**RELEASE**

**BNS-OD-FC001/A4**

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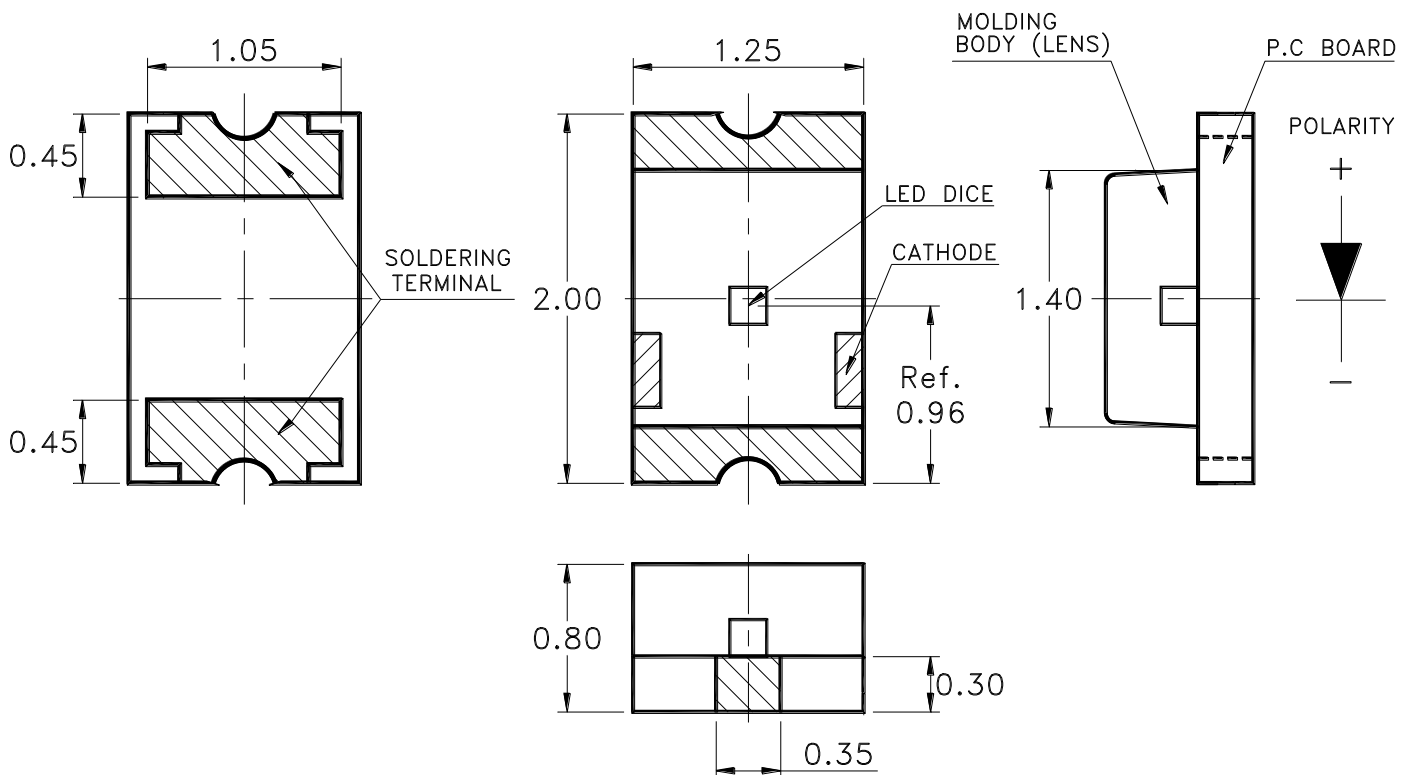
<http://www.liteon.com/opto>

## Features

- \* Meet ROHS, Green Product.
- \* Super thin (0.80H mm) Chip LED.
- \* Package In 8mm Tape On 7" Diameter Reels.
- \* EIA STD package.
- \* I.C. compatible.
- \* Compatible With Automatic Placement Equipment.
- \* Compatible With Infrared And Vapor Phase Reflow Solder Process.



## Package Dimensions



Part No.	Lens	Source Color
LTST-C171TBKT	Water Clear	InGaN Blue

### Notes:

1. All dimensions are in millimeters (inches).
2. Tolerance is  $\pm 0.10$  mm (.004") unless otherwise noted.

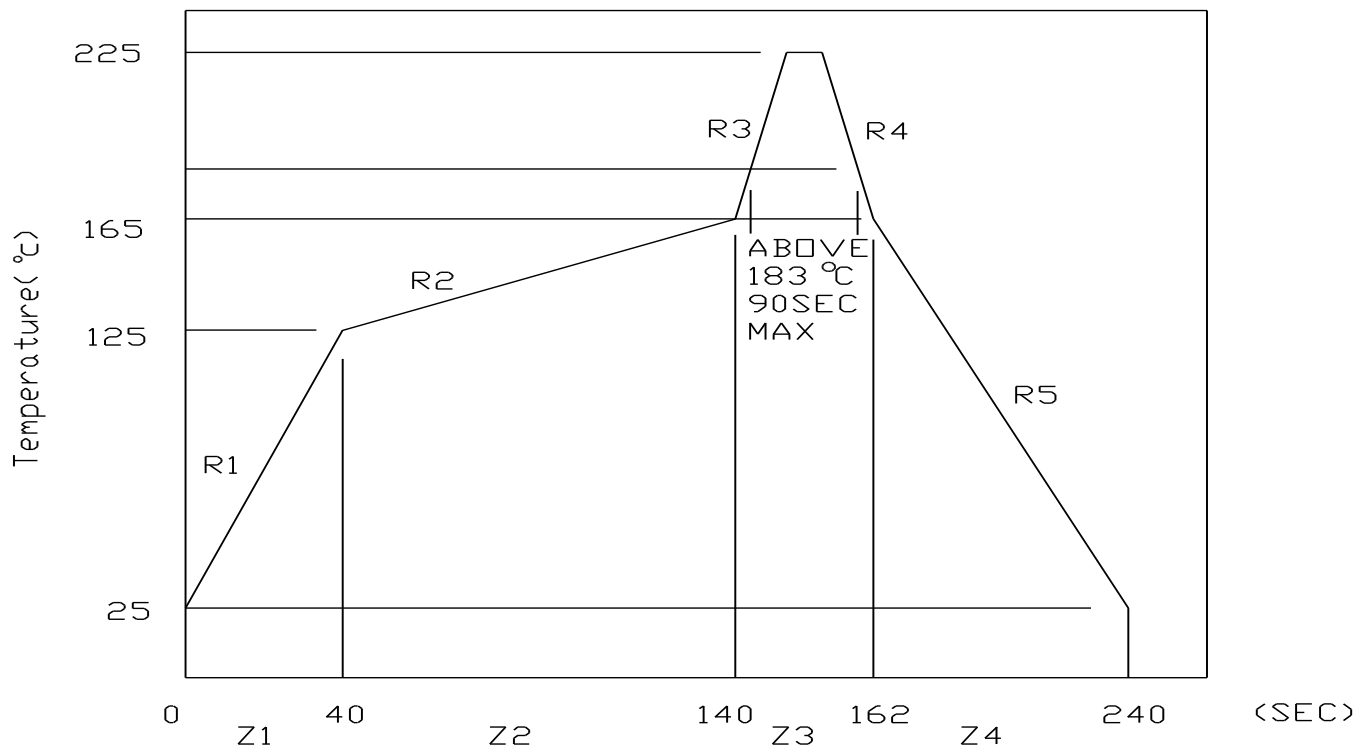
### Absolute Maximum Ratings At Ta=25°C

Parameter	LTST-C171TBKT	Unit
Power Dissipation	76	mW
Peak Forward Current (1/10 Duty Cycle, 0.1ms Pulse Width)	100	mA
DC Forward Current	20	mA
Derating Linear From 30°C	0.25	mA/°C
Reverse Voltage <sup>Note A</sup>	5	V
Operating Temperature Range	-20°C to + 80°C	
Storage Temperature Range	-30°C to + 100°C	
Wave Soldering Condition	260°C For 5 Seconds	
Infrared Soldering Condition	260°C For 5 Seconds	
Vapor Phase Soldering Condition	215°C For 3 Minutes	

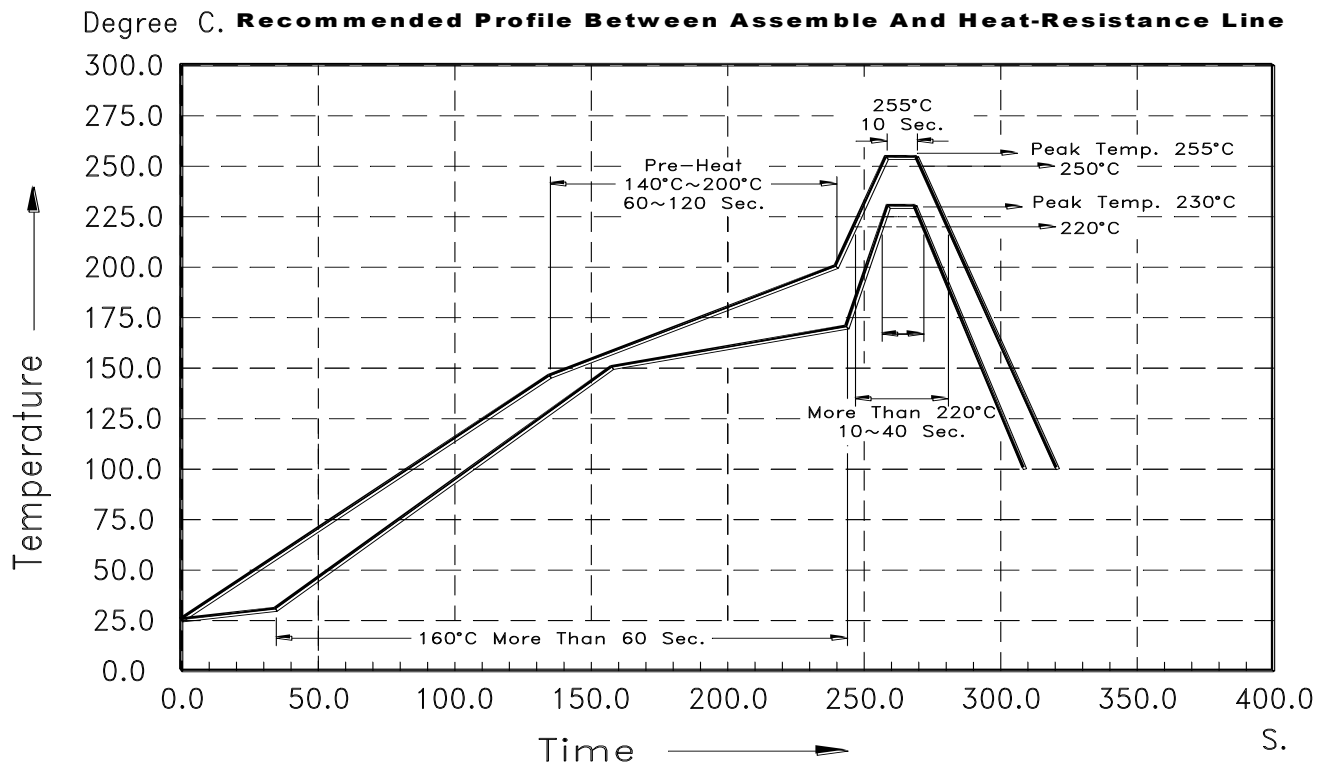
Note A: Reverse Voltage can't be continued operating

### Suggestion Profile:

#### (1) Suggestion IR Reflow Profile For Normal Process



#### (2) Suggestion IR Reflow Profile For Pb Free Process



The Profile is available that must to use SnAg (x=3.3~3.8) Cu (y=0.2~0.7) solder paste

### Electrical Optical Characteristics At Ta=25°C

Parameter	Symbol	Part No. LTST-	Min.	Typ.	Max.	Unit	Test Condition
Luminous Intensity	IV	C171TBKT	28.0	-	180.0	mcd	IF = 20mA Note 1
Viewing Angle	2 $\theta$ 1/2	C171TBKT		130		deg	Note 2 (Fig.6)
Peak Emission Wavelength	$\lambda$ P	C171TBKT		468		nm	Measurement @Peak (Fig.1)
Dominant Wavelength	$\lambda$ d	C171TBKT	465.0	-	475.0	nm	IF = 20mA Note 3
Spectral Line Half-Width	$\Delta \lambda$	C171TBKT		25		nm	
Forward Voltage	VF	C171TBKT	2.80	-	3.80	V	IF = 20mA
Reverse Current	IR	C171TBKT			10	$\mu$ A	VR = 5V

Notes: 1. Luminous intensity is measured with a light sensor and filter combination that approximates the CIE eye-response curve.

2.  $\theta$  1/2 is the off-axis angle at which the luminous intensity is half the axial luminous intensity.

3. The dominant wavelength,  $\lambda$  d is derived from the CIE chromaticity diagram and represents the single wavelength which defines the color of the device.

4. Caution in ESD:

Static Electricity and surge damages the LED. It is recommend to use a wrist band or anti-electrostatic glove when handling the LED. All devices, equipment and machinery must be properly grounded.

**Bin Code List**

Forward Voltage		Unit: V @20mA
Bin Code	Min.	Max.
D7	2.80	3.00
D8	3.00	3.20
D9	3.20	3.40
D10	3.40	3.60
D11	3.60	3.80

Tolerance on each Forward Voltage bin is +/-0.1 volt

Luminous Intensity		Unit : mcd @20mA
Bin Code	Min.	Max.
N	28.0	45.0
P	45.0	71.0
Q	71.0	112.0
R	112.0	180.0

Tolerance on each Intensity bin is +/-15%

Dominant Wavelength		Unit : nm @20mA
Bin Code	Min.	Max.
AC	465.0	470.0
AD	470.0	475.0

Tolerance for each Dominate Wavelength bin is +/- 1nm

### Typical Electrical / Optical Characteristics Curves

(25 °C Ambient Temperature Unless Otherwise Noted)

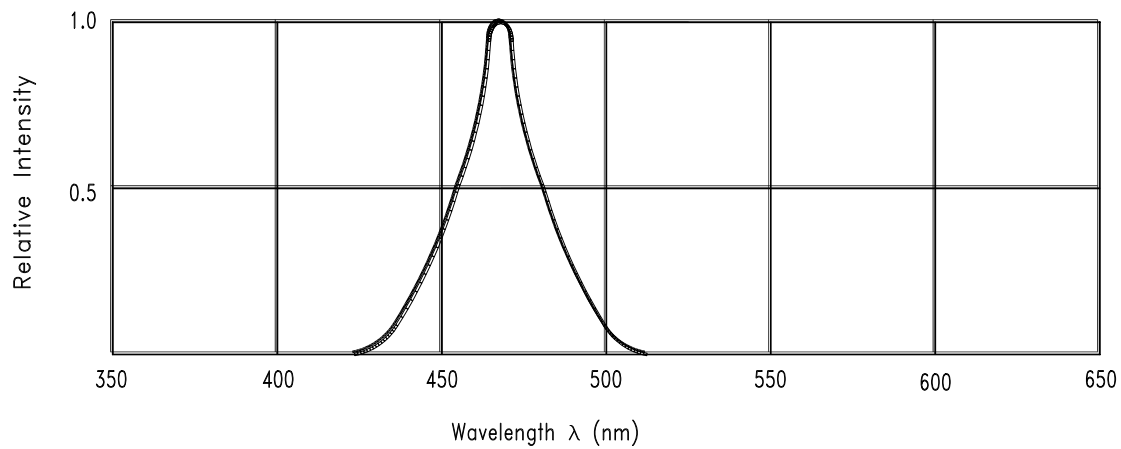


Fig.1 Relative Intensity vs. Wavelength

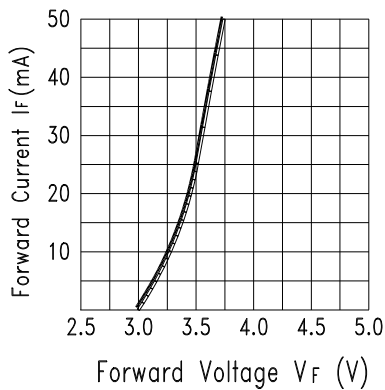


Fig.2 Forward Current vs. Forward Voltage

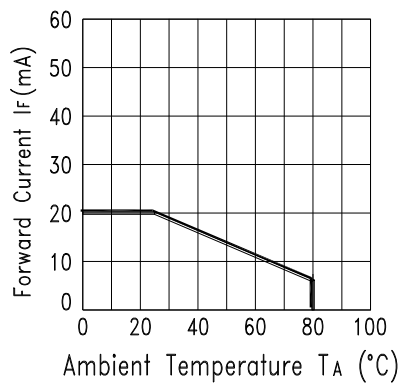


Fig.3 Forward Current Derating Curve

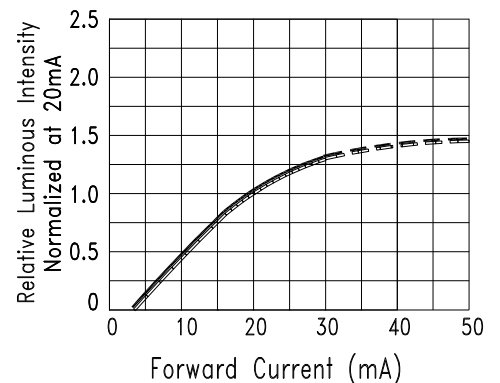


Fig.4 Relative Luminous Intensity vs. Forward Current

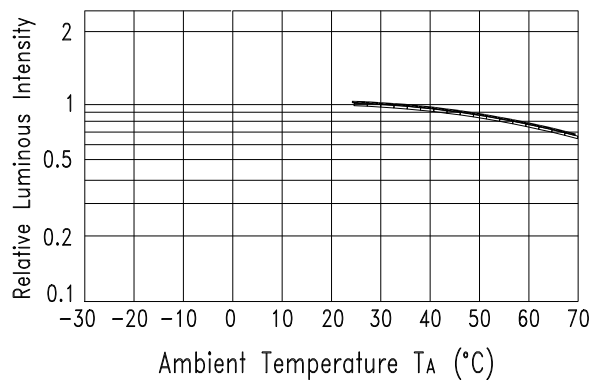


Fig.5 Luminous Intensity vs. Ambient Temperature

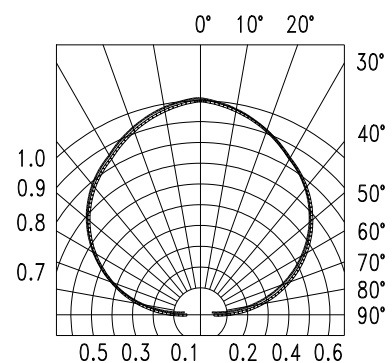


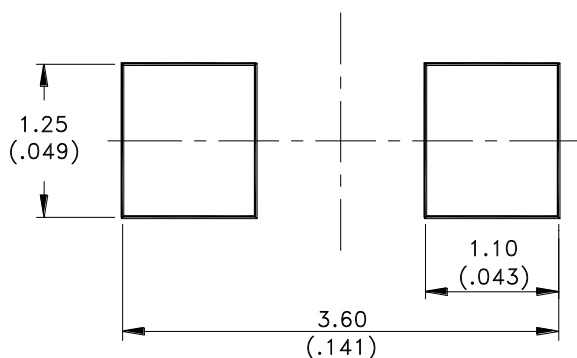
Fig.6 Spatial Distribution

## Cleaning

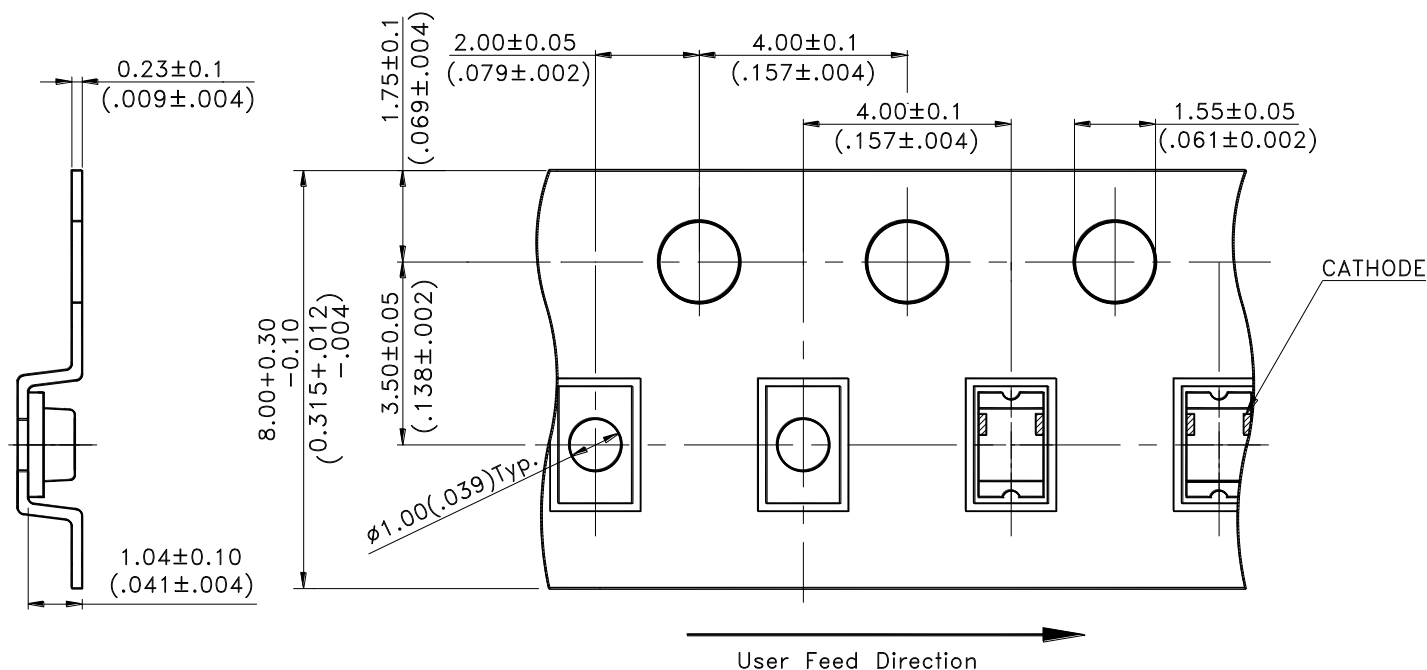
Do not use unspecified chemical liquid to clean LED they could harm the package.

If clean is necessary, immerse the LED in ethyl alcohol or in isopropyl alcohol at normal temperature for less one minute.

## Suggest Soldering Pad Dimensions



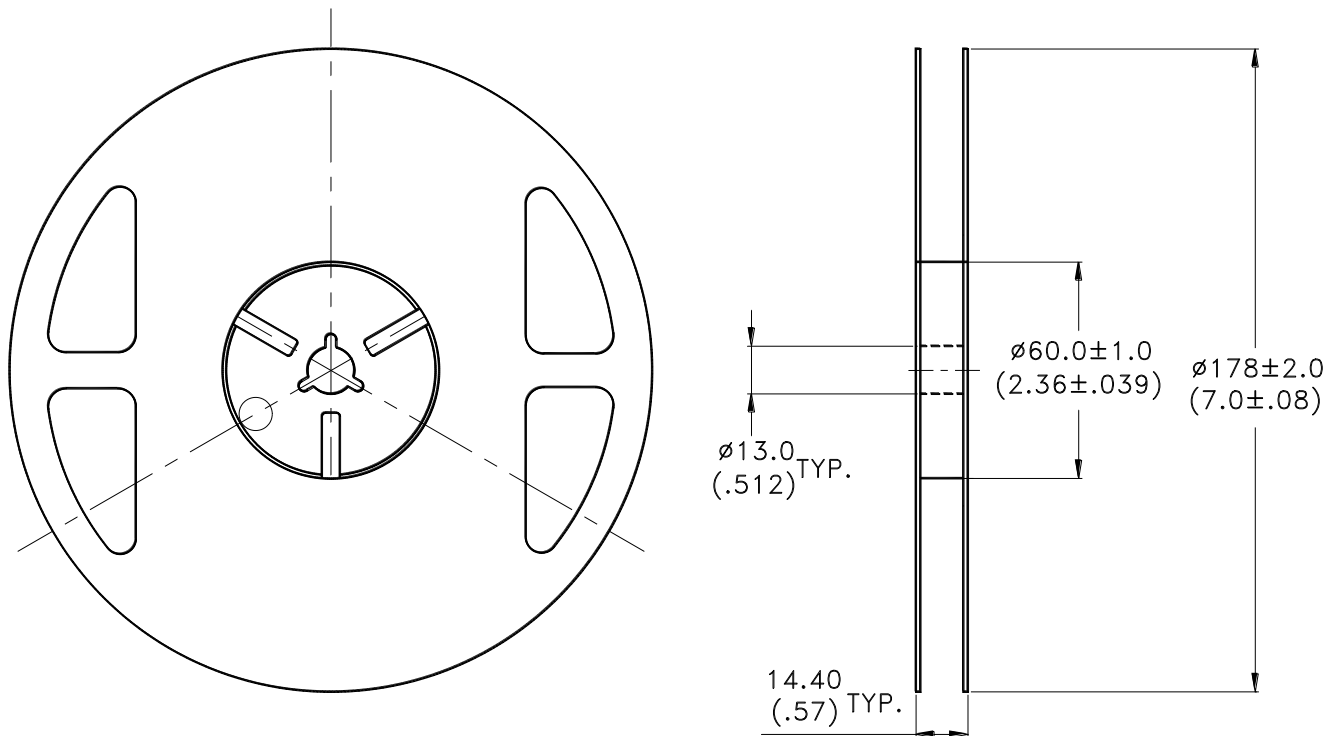
## Package Dimensions Of Tape And Reel



## Notes:

1. All dimensions are in millimeters (inches).





## Notes:

1. Empty component pockets sealed with top cover tape.
2. 7 inch reel-3000 pieces per reel.
3. Minimum packing quantity is 500 pcs for remainders.
4. The maximum number of consecutive missing lamps is two.
5. In accordance with ANSI/EIA 481-1-A-1994 specifications.

## CAUTIONS

### 1. Application

The LEDs described here are intended to be used for ordinary electronic equipment (such as office equipment, communication equipment and household applications). Consult Liteon's Sales in advance for information on applications in which exceptional reliability is required, particularly when the failure or malfunction of the LEDs may directly jeopardize life or health (such as in aviation, transportation, traffic control equipment, medical and life support systems and safety devices).

### 2. Storage

The storage ambient for the LEDs should not exceed 30°C temperature or 70% relative humidity.

It is recommended that LEDs out of their original packaging are IR-reflowed within one week.

For extended storage out of their original packaging, it is recommended that the LEDs be stored in a sealed container with appropriate desiccant, or in a desiccators with nitrogen ambient.

LEDs stored out of their original packaging for more than a week should be baked at about 60 deg C for at least 24 hours before solder assembly.

### 3. Cleaning

Use alcohol-based cleaning solvents such as isopropyl alcohol to clean the LED if necessary.

### 4. Soldering

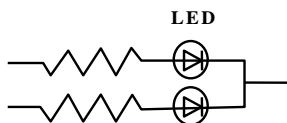
Recommended soldering conditions:

Reflow soldering		Wave Soldering		Soldering iron	
Pre-heat	120~150°C	Pre-heat	100°C Max.	Temperature	300°C Max.
Pre-heat time	120 sec. Max.	Pre-heat time	60 sec. Max.	Soldering time	3 sec. Max.
Peak temperature	260°C Max.	Solder wave	260°C Max.		(one time only)
Soldering time	5 sec. Max.	Soldering time	10 sec. Max.		

### 5. Drive Method

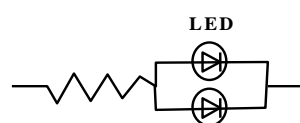
An LED is a current-operated device. In order to ensure intensity uniformity on multiple LEDs connected in parallel in an application, it is recommended that a current limiting resistor be incorporated in the drive circuit, in series with each LED as shown in Circuit A below.

**Circuit model A**



(A) Recommended circuit.

**Circuit model B**



(B) The brightness of each LED might appear different due to the differences in the I-V characteristics of those LEDs.

### 6. ESD (Electrostatic Discharge)

Static Electricity or power surge will damage the LED.

Suggestions to prevent ESD damage:

- Use of a conductive wrist band or anti-electrostatic glove when handling these LEDs.
- All devices, equipment, and machinery must be properly grounded.
- Work tables, storage racks, etc. should be properly grounded.
- Use ion blower to neutralize the static charge which might have built up on surface of the LED's plastic lens as a result of friction between LEDs during storage and handling.

ESD-damaged LEDs will exhibit abnormal characteristics such as high reverse leakage current, low forward voltage, or “ no lightup ” at low currents.

To verify for ESD damage, check for “ lightup ” and Vf of the suspect LEDs at low currents.

The Vf of “ good ” LEDs should be  $>2.0V@0.1mA$  for InGaP product and  $>1.4V@0.1mA$  for AlInGaP product.

### 7. Reliability Test

Classification	Test Item	Test Condition	Reference Standard
Endurance Test	Operation Life	Ta= Under Room Temperature As Per Data Sheet Maximum Rating *Test Time= 1000HRS (-24HRS,+72HRS)*@20mA.	MIL-STD-750D:1026 MIL-STD-883D:1005 JIS C 7021:B-1
	High Temperature High Humidity Storage	IR-Reflow In-Board, 2 Times Ta= $65\pm5^{\circ}C$ , RH= 90~95% *Test Time= 240HRS $\pm$ 2HRS	MIL-STD-202F:103B JIS C 7021:B-11
	High Temperature Storage	Ta= $105\pm5^{\circ}C$ *Test Time= 1000HRS (-24HRS,+72HRS)	MIL-STD-883D:1008 JIS C 7021:B-10
	Low Temperature Storage	Ta= $-55\pm5^{\circ}C$ *Test Time=1000HRS (-24HRS,+72HRS)	JIS C 7021:B-12
Environmental Test	Temperature Cycling	$105^{\circ}C \sim 25^{\circ}C \sim -55^{\circ}C \sim 25^{\circ}C$ 30mins 5mins 30mins 5mins 10 Cycles	MIL-STD-202F:107D MIL-STD-750D:1051 MIL-STD-883D:1010 JIS C 7021:A-4
	Thermal Shock	IR-Reflow In-Board, 2 Times $85\pm5^{\circ}C \sim -40^{\circ}C \pm 5^{\circ}C$ 10mins 10mins 10 Cycles	MIL-STD-202F:107D MIL-STD-750D:1051 MIL-STD-883D:1011
	Solder Resistance	T.sol= $260\pm5^{\circ}C$ Dwell Time= $10\pm1$ secs	MIL-STD-202F:210A MIL-STD-750D:2031 JIS C 7021:A-1
	IR-Reflow Normal Process	Ramp-up rate( $183^{\circ}C$ to Peak) $+3^{\circ}C$ / second max Temp. maintain at $125(\pm25)^{\circ}C$ 120 seconds max Temp. maintain above $183^{\circ}C$ 60-150 seconds Peak temperature range $235^{\circ}C+5/-0^{\circ}C$ Time within $5^{\circ}C$ of actual Peak Temperature (tp) 10-30 seconds Ramp-down rate $+6^{\circ}C$ /second max	MIL-STD-750D:2031.2 J-STD-020C
	IR-Reflow Pb Free Process	Ramp-up rate( $217^{\circ}C$ to Peak) $+3^{\circ}C$ / second max Temp. maintain at $175(\pm25)^{\circ}C$ 180 seconds max Temp. maintain above $217^{\circ}C$ 60-150 seconds Peak temperature range $260^{\circ}C+0/-5^{\circ}C$ Time within $5^{\circ}C$ of actual Peak Temperature (tp) 20-40 seconds Ramp-down rate $+6^{\circ}C$ /second max	MIL-STD-750D:2031.2 J-STD-020C
	Solderability	T.sol= $235\pm5^{\circ}C$ Immersion time $2\pm0.5$ sec Immersion rate $25\pm2.5$ mm/sec Coverage $\geq 95\%$ of the dipped surface	MIL-STD-202F:208D MIL-STD-750D:2026 MIL-STD-883D:2003 IEC 68 Part 2-20 JIS C 7021:A-2

### 8. Others

The appearance and specifications of the product may be modified for improvement without prior notice.

**9. Suggested Checking List****Training and Certification**

1. Everyone working in a static-safe area is ESD-certified?
2. Training records kept and re-certification dates monitored?

**Static-Safe Workstation & Work Areas**

1. Static-safe workstation or work-areas have ESD signs?
2. All surfaces and objects at all static-safe workstation and within 1 ft measure less than 100V?
3. All ionizer activated, positioned towards the units?
4. Each work surface mats grounding is good?

**Personnel Grounding**

1. Every person (including visitors) handling ESD sensitive (ESDS) items wears wrist strap, heel strap or conductive shoes with conductive flooring?
2. If conductive footwear used, conductive flooring also present where operator stand or walk?
3. Garments, hairs or anything closer than 1 ft to ESD items measure less than 100V\*?
4. Every wrist strap or heel strap/conductive shoes checked daily and result recorded for all DLs?
5. All wrist strap or heel strap checkers calibration up to date?

Note: \*50V for Blue LED.

**Device Handling**

1. Every ESDS items identified by EIA-471 labels on item or packaging?
2. All ESDS items completely inside properly closed static-shielding containers when not at static-safe workstation?
3. No static charge generators (e.g. plastics) inside shielding containers with ESDS items?
4. All flexible conductive and dissipative package materials inspected before reuse or recycles?

**Others**

1. Audit result reported to entity ESD control coordinator?
2. Corrective action from previous audits completed?
3. Are audit records complete and on file?